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PATENT

JC971 U.S. PTO
09/866334
05/25/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Embabi et al. (TI-31516)

Serial No. __/__/__

Group Art Unit:

Filed: Herewith

Examiner:

For: Variable Capacitor for Tuned Circuits

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, DC 20231

Dear Sir:

Applicants wish to bring the reference listed on the enclosed PTO-1449 to the attention of the Patent and Trademark Office relative to the subject application.

A copy of the reference is enclosed. The cited reference is in the English language. As such, no additional statement of relevance is provided in this paper.

Consideration of this information in the prosecution of this application is respectfully requested.

Respectfully submitted,

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Sheet 1 of 1

U.S. Department of Commerce Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Atty. Docket No. TI-31516	Serial Number
	Applicants: Embabi et al.	
	Filing Date Herewith	Group

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U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Sub Class	Filing Date if appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Sub Class	Translation --- --- Yes No
	AL						
	AM						
	AN						
	AO						
	AP						

Other References (Including Author, Title, Date, Pertinent Pages, Etc.)

AQ	Parker, et al., "A 1.6 GHz CMOS PLL with On-Chip Loop Filter", J. Solid State Circ., Vol. 33, No. 3 (IEEE, March 1998), pp. 337-343
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